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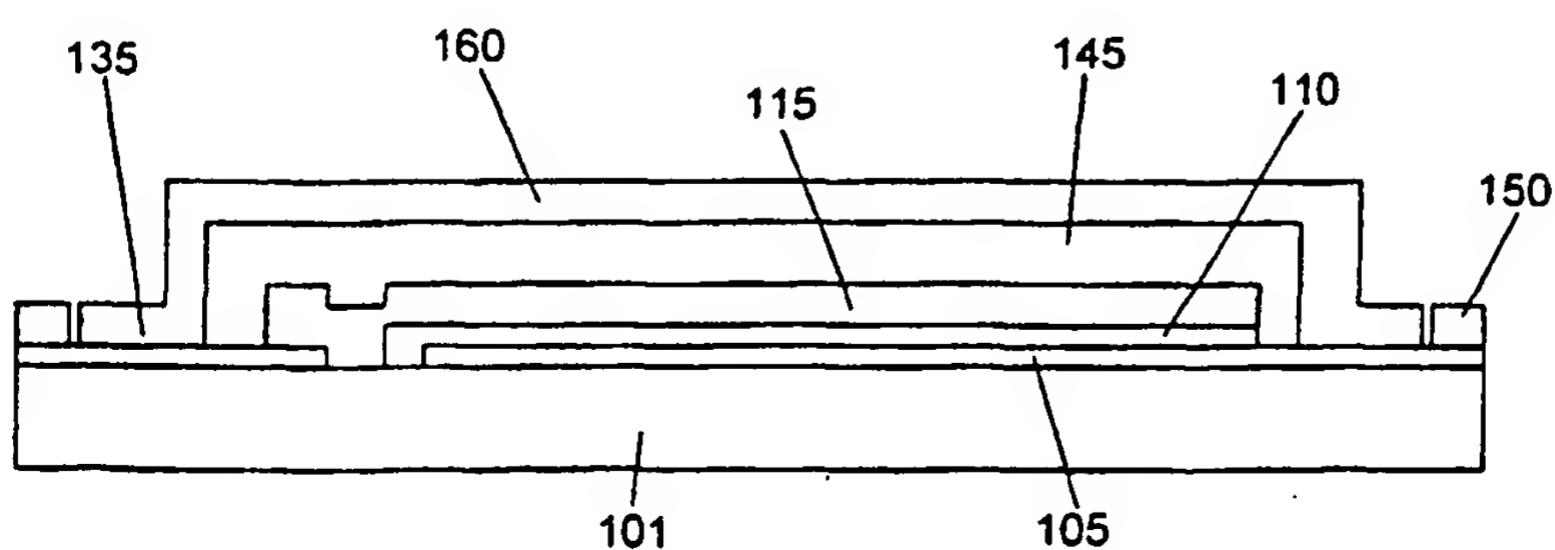
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(54) Title: IMPROVED TRANSPARENT ELECTRODE MATERIAL FOR QUALITY ENHANCEMENT OF OLED DEVICES

(57) Abstract: A transparent conductive material (101) in which the desired resistivity is achieved with a high carrier concentration is provided for use in an OLED. In one embodiment, the transparent conductive material (101) comprises indium-tin-oxide (ITO) with a high carrier concentration of at least $7 \times 10^{20} \text{ cm}^{-3}$. The high carrier concentration improved the performance of the OLED device.**WO 01/45182 A1**

IMPROVED TRANSPARENT ELECTRODE MATERIAL FOR
QUALITY ENHANCEMENT OF OLED DEVICES

Field of the Invention

5 The present invention relates to organic light emitting diode (OLED) devices. More particularly, the invention relates to an improved transparent electrode material for use in OLED devices.

10 Background of the Invention

OLED devices are used, for example, to form displays such as flat panel displays (FPDs). The displays are used in many different products such as pagers, cellular phones, and personal organizers.

15 Typically, an OLED comprises one or more organic functional layers sandwiched between two electrodes. Charge carriers are injected through the electrodes and recombine in the functional layers, thereby emitting visible radiation.

20 One of the electrodes is formed from a transparent conductive material, enabling the radiation to be seen. The transparent conductive material should possess low resistivity, high optical transmittance, and appropriate

work function to produce an OLED device with adequate performance.

A transparent electrode material that is useful in OLED applications is indium-tin-oxide (ITO) due to its 5 high transparency in the visible wavelength range. ITO is commonly used in liquid crystal display (LCD) applications. However, resistivity and work function of the ITO used in LCD applications do not meet the requirements for OLED applications. This leads to lower 10 performance in OLED devices, making portable applications which operate on battery power impractical.

Also, the surface morphology (roughness) of ITO layers used in LCD is not suited for OLED applications. Typically, LCD applications require the surface of the 15 ITO to be rough in order to promote adhesion of the polyimide coating. The rough ITO surface produces high electric fields, which can be detrimental for OLEDs. For example, the high electric fields can induce 20 electrical shorts since the electrodes are only separated by a distance of about 100 - 200 nm (the usual thickness of the organic functional layer stack).

As evidenced from the above discussion, it is desirable to provide a transparent conductive layer that meets the needs for OLED applications.

Summary of the Invention

The invention relates to organic LED (OLED) devices. The OLED device includes an OLED stack in the 5 device region of a substrate. The OLED stack comprises first and second conductive electrodes separated by at least one organic functional layer. One of the electrodes is formed from a transparent conductive material. In accordance with the invention, the 10 transparent conductive material comprises a desired resistivity achieved with a high carrier concentration. Providing the transparent conductive material having the desired resistivity achieved with a high carrier concentration has been found to improve device 15 performance. In one embodiment, the carrier concentration of the transparent conductive layer is at least $7 \times 10^{20} \text{ cm}^{-3}$.

20 Brief Description of the Drawings

Fig. 1 shows an embodiment of the invention; and Figs. 2- 6 show a process for fabricating an OLED device in accordance with one embodiment of the invention.

Preferred Embodiments of the Invention

The invention relates generally to OLED devices.

In particular, the invention provides a transparent conductive material that increases the performance of 5 OLED devices. In one embodiment, the transparent conductive material comprises indium-tin-oxide (ITO). Other types of transparent conductive materials, such as indium-zinc-oxide, zinc-oxide, tin-oxide, are useful.

We have discovered that for a given resistivity, 10 the concentration of charge carriers in the transparent conductive material can impact the performance of the OLED device. The concentration of charge carriers in the transparent conductive material has a direct relationship to the performance of the OLED device. In 15 one embodiment, the transparent conductive material comprises a high charge carrier concentration to improve the efficiency of the OLED device. Preferably, the transparent conductive material comprises as high a charge carrier concentration as possible.

20 Fig. 1 shows an OLED device 100 in accordance with one embodiment of the invention. The OLED device comprises one or more organic functional layers 110 between first and second electrodes 105 and 115 formed on a substrate 101. The substrate, in one embodiment

comprises a transparent material. The electrodes can be patterned to form, for example, a plurality of OLED cells to create a pixelated OLED device. Bond pads 150, which are coupled to the first and second electrodes, 5 are provided to enable electrical connections to the OLED cells. A cap 160 is provided to encapsulate the device.

One of the electrodes comprises a transparent conductive material. Typically, the first electrode 10 which, for example, is on the transparent substrate 101 is transparent. The transparent conductive material, in one embodiment, comprises ITO. The resistivity in the ITO is sufficiently low to satisfy the requirements for OLED applications. In one embodiment, the resistivity 15 of the ITO is less than or equal to about 4×10^{-4} $\Omega\text{-cm}$. The resistivity is related to carrier mobility and carrier concentration of the material.

In accordance with the invention, the desired resistivity of the ITO is achieved with a high carrier 20 concentration. In one embodiment, the carrier concentration of the ITO is at least about 7×10^{20} cm^{-3} . The high carrier concentration has been found to improve performance in the OLED device. This results in lower

power consumption, making OLED applications more feasible for portable applications.

Figs. 2-6 show a process for fabricating an OLED device in accordance with one embodiment of the 5 invention. Referring to Fig. 2, a substrate 201 is provided on which OLED cell or cells are formed. In one embodiment, a transparent substrate is provided. The substrate usually comprises soda lime or borosilicate glass. Other types of glass materials can also be used 10 to serve as the substrate. Typically, the substrate is about 0.4 - 1.1 mm thick. The use of thinner substrates, for example about 20 - 300 μm , is also useful.

A transparent conductive layer 205 is deposited on 15 the substrate surface. The transparent conductive material, in one embodiment comprises ITO. Other transparent conductive materials, such as indium-zinc-oxide, zinc-oxide, tin-oxide, can also be useful. The thickness of the ITO typically is about 40 - 400 nm. 20 Processing problems may arise if the ITO is too thick. In one embodiment, the ITO is less than or equal to 150 nm thick. Preferably, the ITO layer is about 120 to 150 nm thick. The resistivity of the ITO is less than or equal to about $4 \times 10^{-4} \Omega\text{cm}$.

In one embodiment, the ITO layer is sputtered onto the substrate using, for example, a radio frequency magnetron sputtering tool. The deposition parameters, such as temperature, pressure, process gas mixture, and 5 deposition rate, are controlled such that the resistivity of the deposited ITO layer is achieved with a high carrier concentration. In one embodiment, the charge carrier concentration of the ITO is at least about $7 \times 10^{20} \text{ cm}^{-3}$. Preferably, the ITO comprises as high 10 a charge carrier concentration as possible. The high carrier concentration enhances hole-injection, leading to an increase in electroluminescent efficiency.

The ITO is sputtered using an oxidized target comprising, for example, In_2O_3 and SnO_2 . The weight 15 proportion of the In_2O_3 and SnO_2 in the target is about 9:1. Other compositions and types of targets can also be used. The deposition parameters of the sputtering process can be as follows:

	substrate temperature	: 300 - 400 °C
20	processing pressure	: $10^{-3} - 10^{-5}$ Torr
	processing gas mixture	: Ar, H_2
	deposition rate	: 1 - 10 nm/min;
		preferably 1 - 2 nm/min

Alternative deposition techniques, such as chemical vapor deposition (CVD), plasma enhanced CVD, or laser ablation, may also be useful in depositing the ITO layer

The deposited ITO layer comprises good optical 5 characteristics and appropriate work function to satisfy the requirements of OLED applications. In one embodiment, the ITO has an optical transmittance in the visible wavelength range of over 85%. The work function of the ITO should closely match the ionization potential 10 of the subsequently formed organic functional layers.

In one embodiment, the ITO comprises a work function of about 4.8 - 5.2 eV.

Preferably, the surface of the ITO is relatively smooth to reduce the presence of electric fields which 15 may lead to shorts. In one embodiment, the root mean square (RMS) roughness of the ITO surface is less than about 2 nm to reduce the possibility of shorts caused by strong electric fields.

Referring to Fig 3, the conductive layer 205 is 20 patterned as desired to selectively remove portions of the layer, exposing portions 356 of the substrate. The patterned conductive layer serves as for example, the anode for the OLED cells. In one embodiment, the conductive layer is patterned to form strips that serve

the anode of a pixelated OLED device. The patterning process can also form connections for bond pads.

One or more functional organic layers 310 are deposited on the substrate, covering the exposed 5 substrate portions and the conductive layer. The functional organic layers comprise, for example, conjugated polymer or Alq₃. Other types of functional organic layers are also useful. The thickness of the organic layers is typically about 2 - 200 nm.

10 Referring to Fig. 4, portions of the organic layers are selectively removed, for example, to expose underlying layers in regions 470 for bond pad connections. Selective removal of the organic layers can be achieved by a polishing process. Other 15 techniques, such as etching, scratching, or laser ablation, can also be used to selectively remove portions of the organic layers.

Referring to Fig. 5, a second conductive layer 515 is deposited on the substrate. The conductive layer 20 comprises, for example, Ca, Mg, Ba, Ag, Al or a mixture or alloy thereof. Other conductive materials, particularly those comprising a low work function, can also be used to form the second conductive layer. In one embodiment, the second conductive layer is patterned

to form electrode strips that serve as cathodes for a pixelated OLED device. Also, connections for bond pads can be formed during the patterning process.

Alternatively, the conductive layer can be selectively deposited to form cathode strips and bond pad connections. Selective deposition of the conductive layer can be achieved with, for example, a mask layer. The cathode strips are typically orthogonal to the anode strips. Forming cathode strips that are diagonal to the anode strips is also useful. The intersections of the top and bottom electrode strips form organic LED pixels.

Referring to Fig. 6, a cap 660 is mounted on the substrate to encapsulate the device. The cap layer comprises, for example, metal or glass. Other types of cap which protect the active components from the environment, such as ceramic or metallized foil, are also useful. Various techniques can be used to mount the cap layer. In one embodiment, an adhesive is used to mount the cap layer. Adhesives such as self-hardening adhesives, UV or thermal curable adhesives, or hot melt adhesives are useful. Other techniques which employ low temperature solder materials, ultrasonic bonding, or welding techniques using inductance or laser welding are also useful. When mounted, the cap creates

a cavity 645, providing separation between it and the OLED cells. Bond pads 650 are formed to provide electrical access to the OLED cells.

5

Experiments

A first OLED device was fabricated using a fluorine based polymeric organic functional material. The organic functional material was sandwiched between a transparent conductive anode and a metallic cathode.

10 The metallic cathode comprised a 50 nm thick Ca layer covered by a 200 nm thick Ag layer. The transparent conductive anode comprised ITO with a resistivity of $2.7 \times 10^{-4} \Omega \text{cm}$ and a charge carrier concentration of $9 \times 10^{20} \text{ cm}^{-3}$.

15 The ITO was deposited on a glass substrate using a radio frequency magnetron sputter in argon-hydrogen mixture. An oxidized target with In_2O_3 and SnO_2 having a weight proportion of 9:1 was used. The base pressure of the system was about 5.0×10^{-8} Torr. The total pressure

20 of the sputtering gas mixture was adjusted to 3.0×10^{-3} Torr during deposition. The hydrogen partial pressure was about $7-9 \times 10^{-5}$ Torr. The ITO was deposited at the temperature of about $300-400^\circ\text{C}$.

A second OLED device was fabricated. The second OLED device was identical to the first except the ITO had a resistivity of $3.2 \times 10^{-4} \Omega \text{cm}$ and a charge carrier concentration of $5 \times 10^{20} \text{ cm}^{-3}$. This ITO film was prepared with only argon during the sputtering process. The rest of the deposition parameters were kept the same as those used for producing the first OLED device.

An experiment was conducted to compare the efficiency of the two OLED devices. A linear bias ramp was applied to the devices while simultaneously monitoring OLED current as well as luminance. The device efficiency was determined by dividing luminance and current density. The first device with the high carrier concentration of $9 \times 10^{20} \text{ cm}^{-3}$ achieved a maximum efficiency of 4.14 cd/A. This was 1.5 times higher than the maximum efficiency achieved with the second device having a lower carrier concentration of $5 \times 10^{20} \text{ cm}^{-3}$.

While the invention has been particularly shown and described with reference to various embodiments, it will be recognized by those skilled in the art that modifications and changes may be made to the present invention without departing from the spirit and scope thereof. The scope of the invention should therefore be determined not with reference to the above description

but with reference to the appended claims along with their full scope of equivalents.

What is claimed is:

1. An organic LED (OLED) device comprising:
 - a substrate with a device region; and
 - an OLED stack in the device region, wherein the

5 OLED stack comprises,
 - a transparent conductive layer serving as a first electrode, the transparent conductive layer comprising a resistivity achieved with a high carrier concentration,

10 a conductive layer serving as a second electrode, and

 - at least one organic functional layer separating the first and second electrodes.
- 15 2. The device of claim 1 wherein the resistivity of the transparent conductive layer is less than or equal to about 4×10^{-4} Ωcm .
3. The device of claim 2 wherein the transparent
- 20 conductive layer comprises indium-tin-oxide.
4. The device of claim 2 wherein the transparent conductive layer comprises a material selected from the

group consisting of indium-zinc-oxide, zinc-oxide, or tin-oxide.

5. The device of claim 1, 2, 3, or 4 wherein the
5 carrier concentration is at least about $7 \times 10^{20} \text{ cm}^{-3}$.

6. The device of claim 5 wherein the transparent
conductive layer comprises an optical transmittance in
the visible wavelength range $> 85\%$.

10

7. The device of claim 5 wherein the transparent
conductive layer comprises a work function closely
matching an ionization potential of the organic
functional layer.

15

8. The device of claim 5 wherein a surface of the
transparent conductive layer comprises a RMS roughness $< 2 \text{ nm}$.

20 9. The device of claim 5 wherein the transparent
conductive layer comprises an optical transmittance in
the visible wavelength range $> 85\%$, a work function
closely matching an ionization potential of the organic
functional layer, a RMS surface roughness $< 2 \text{ nm}$.

10. The device of claim 1, 2, 3, or 4 wherein the carrier concentration is about 7×10^{20} cm⁻³ to about 9×10^{20} cm⁻³.

5

11. The device of claim 10 wherein the transparent conductive layer comprises an optical transmittance in the visible wavelength range > 85%.

10 12. The device of claim 10 wherein the transparent conductive layer comprises a work function closely matching an ionization potential of the organic functional layer.

15 13. The device of claim 10 wherein a surface of the transparent conductive layer comprises a RMS roughness < 2 nm.

14. The device of claim 10 wherein the transparent 20 conductive layer comprises an optical transmittance in the visible wavelength range > 85%, a work function closely matching an ionization potential of the organic functional layer, a RMS surface roughness < 2 nm.

15. A method for fabricating an organic LED (OLED) device comprising:
 - providing a substrate;
 - depositing a transparent conductive layer on the substrate, wherein the transparent conductive layer comprises a resistivity achieved with a high carrier concentration; and
 - processing the substrate to form the OLED device.
- 10 16. The device of claim 15 wherein the processing comprises:
 - depositing patterning the transparent conductive layer to serve as a first electrode;
 - depositing a functional organic layer over the first electrode;
 - depositing a conductive layer over the organic functional layer;
 - patterning the conductive layer to form a second electrode, wherein the functional organic layer between patterned first and second electrode forms OLED cells;
 - 20 and
 - mounting a cap on the substrate to encapsulate the OLED device.

17. The device of claim 16 wherein the resistivity of the transparent conductive layer is less than or equal to about 4×10^{-4} Ω cm.

5 18. The device of claim 17 wherein the transparent conductive layer comprises indium-tin-oxide.

19. The device of claim 17 wherein the transparent conductive layer comprises a material selected from the 10 group consisting of indium-zinc-oxide, zinc-oxide, or tin-oxide.

20. The device of claim 15 wherein the resistivity of the transparent conductive layer is less than or equal 15 to about 4×10^{-4} Ω cm.

21. The device of claim 20 wherein the transparent conductive layer comprises indium-tin-oxide.

20 22. The device of claim 20 wherein the transparent conductive layer comprises a material selected from the group consisting of indium-zinc-oxide, zinc-oxide, or tin-oxide.

23. The device of claim 15 wherein the transparent conductive layer comprises indium-tin-oxide.

24. The device of claim 15 wherein the transparent 5 conductive layer comprises a material selected from the group consisting of indium-zinc-oxide, zinc-oxide, or tin-oxide.

25. The device of claim 15, 16, 17, 18, 19, 20, 21, 22, 10 23, or 24 wherein the carrier concentration is at least about $7 \times 10^{20} \text{ cm}^{-3}$.

26. The method of claim 25 wherein depositing the transparent conductive layer comprises sputtering the 15 transparent conductive layer on the substrate.

27. The method of claim 26 wherein the transparent conductive layer comprises an optical transmittance in the visible wavelength range > 85%.

20

28. The method of claim 26 wherein the transparent conductive layer comprises a work function closely matching an ionization potential of the organic functional layer.

29. The method of claim 26 wherein a surface of the transparent conductive layer comprises a RMS roughness < 2 nm.

5

30. The method of claim 26 wherein the transparent conductive layer comprises an optical transmittance in the visible wavelength range > 85%, a work function closely matching an ionization potential of the organic 10 functional layer, a RMS surface roughness < 2 nm.

31. The method of claim 15, 16, 17, 18, 19, 20, 21, 22, 23, or 24 wherein the carrier concentration is about $7 \times 10^{20} \text{ cm}^{-3}$ to about $9 \times 10^{20} \text{ cm}^{-3}$.

15

32. The method of claim 31 wherein depositing the transparent conductive layer comprises sputtering the transparent conductive layer on the substrate.

20 33. The method of claim 32 wherein the transparent conductive layer comprises an optical transmittance in the visible wavelength range > 85%.

34. The method of claim 32 wherein the transparent conductive layer comprises a work function closely matching an ionization potential of the organic functional layer.

5

35. The method of claim 32 wherein a surface of the transparent conductive layer comprises a RMS roughness < 2 nm.

10 36. The method of claim 32 wherein the transparent conductive layer comprises an optical transmittance in the visible wavelength range > 85%, a work function closely matching an ionization potential of the organic functional layer, a RMS surface roughness < 2 nm.

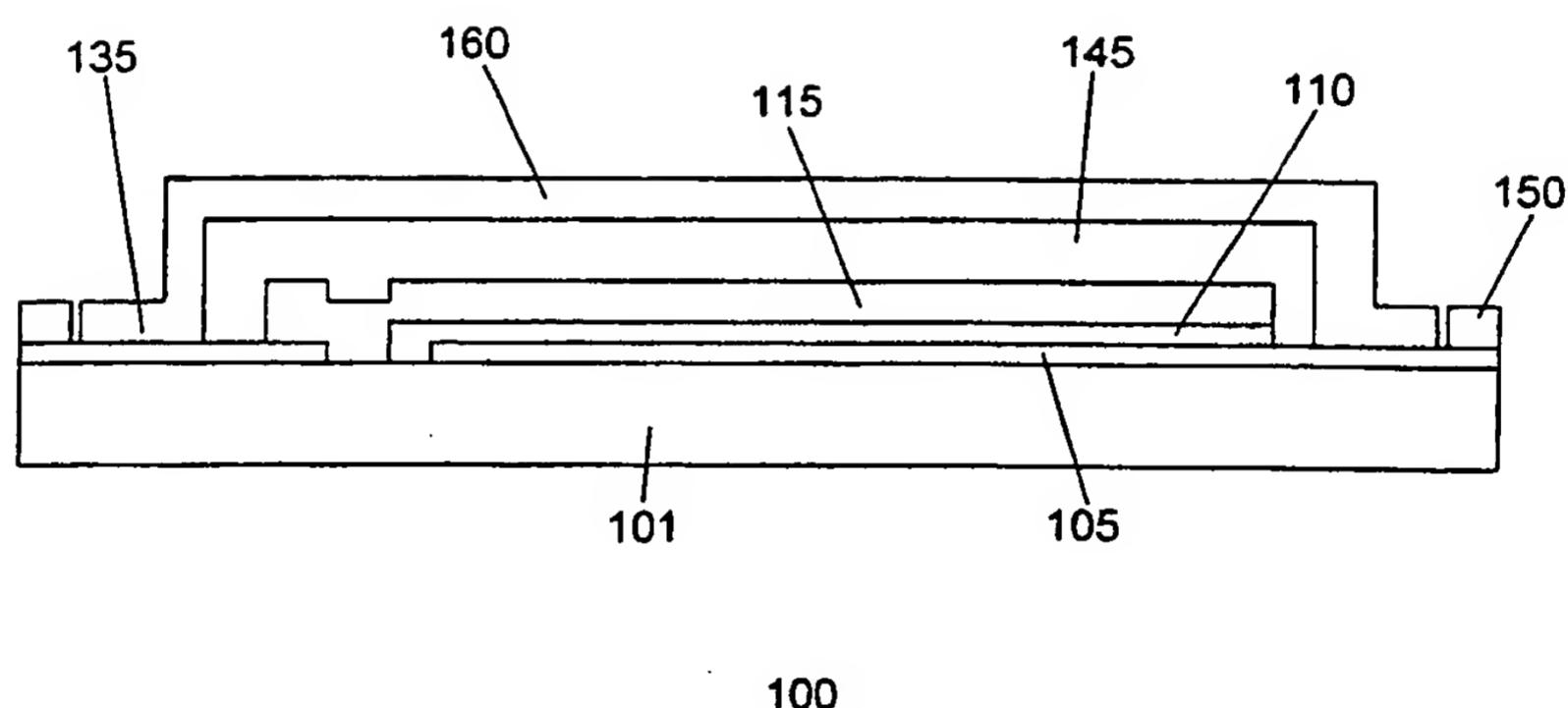


Fig. 1

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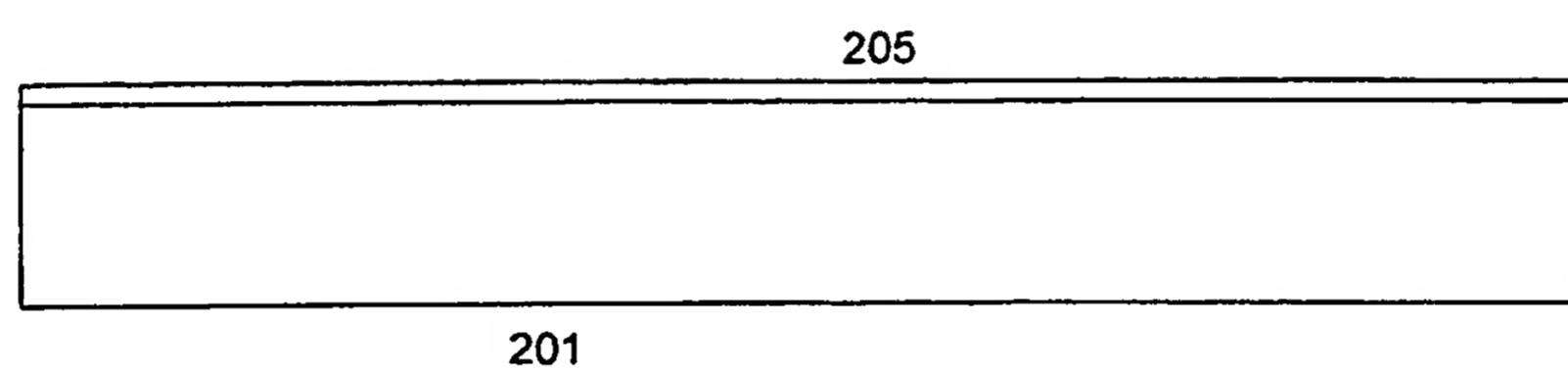


Fig. 2

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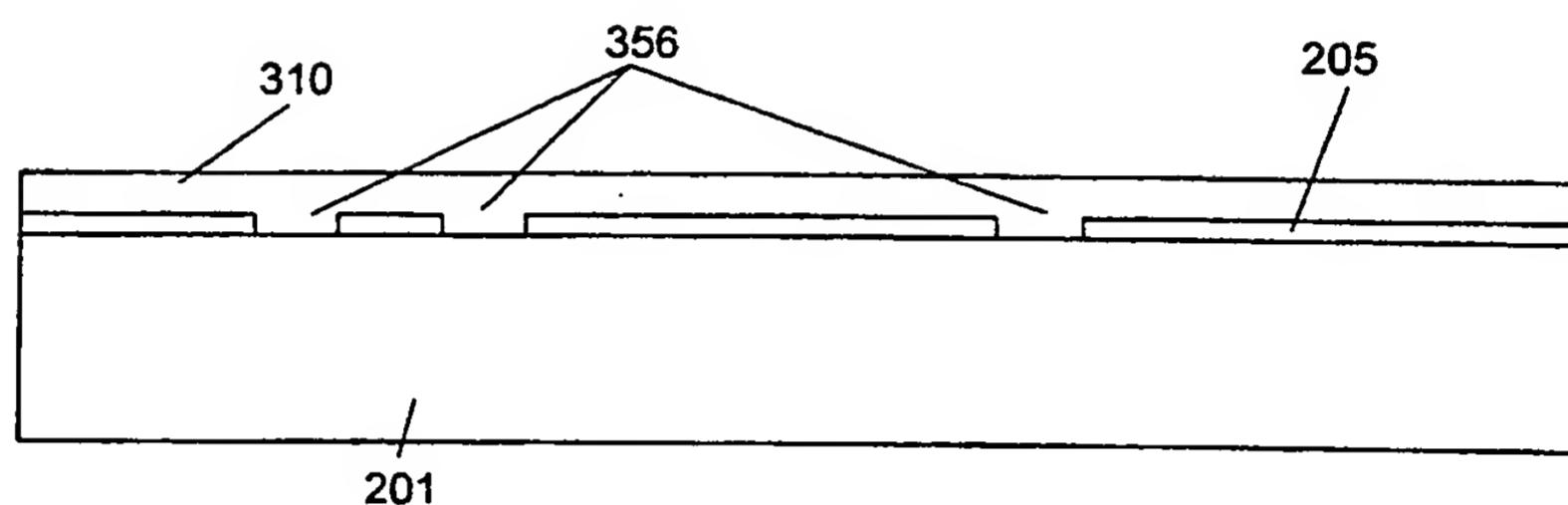


Fig. 3

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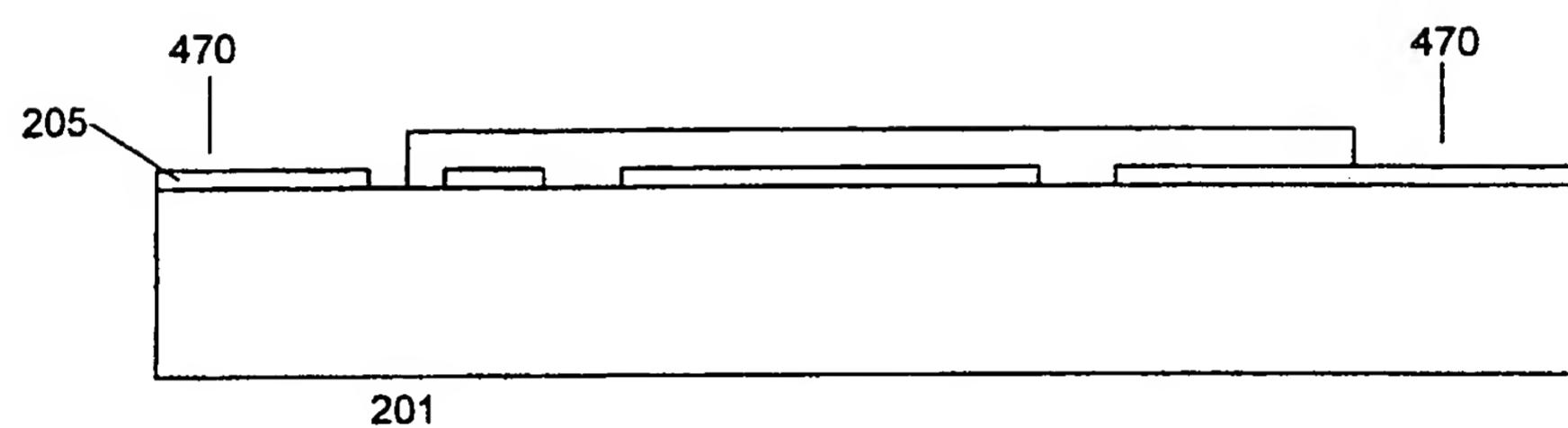


Fig. 4

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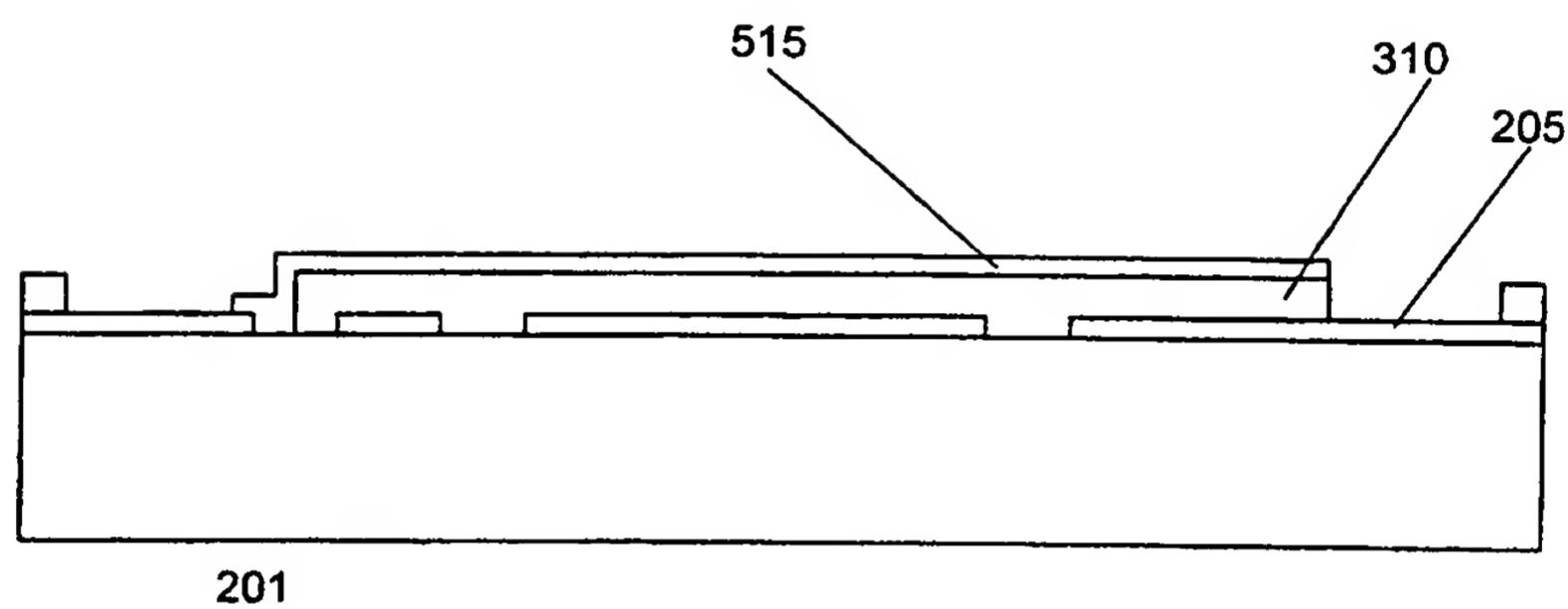


Fig. 5

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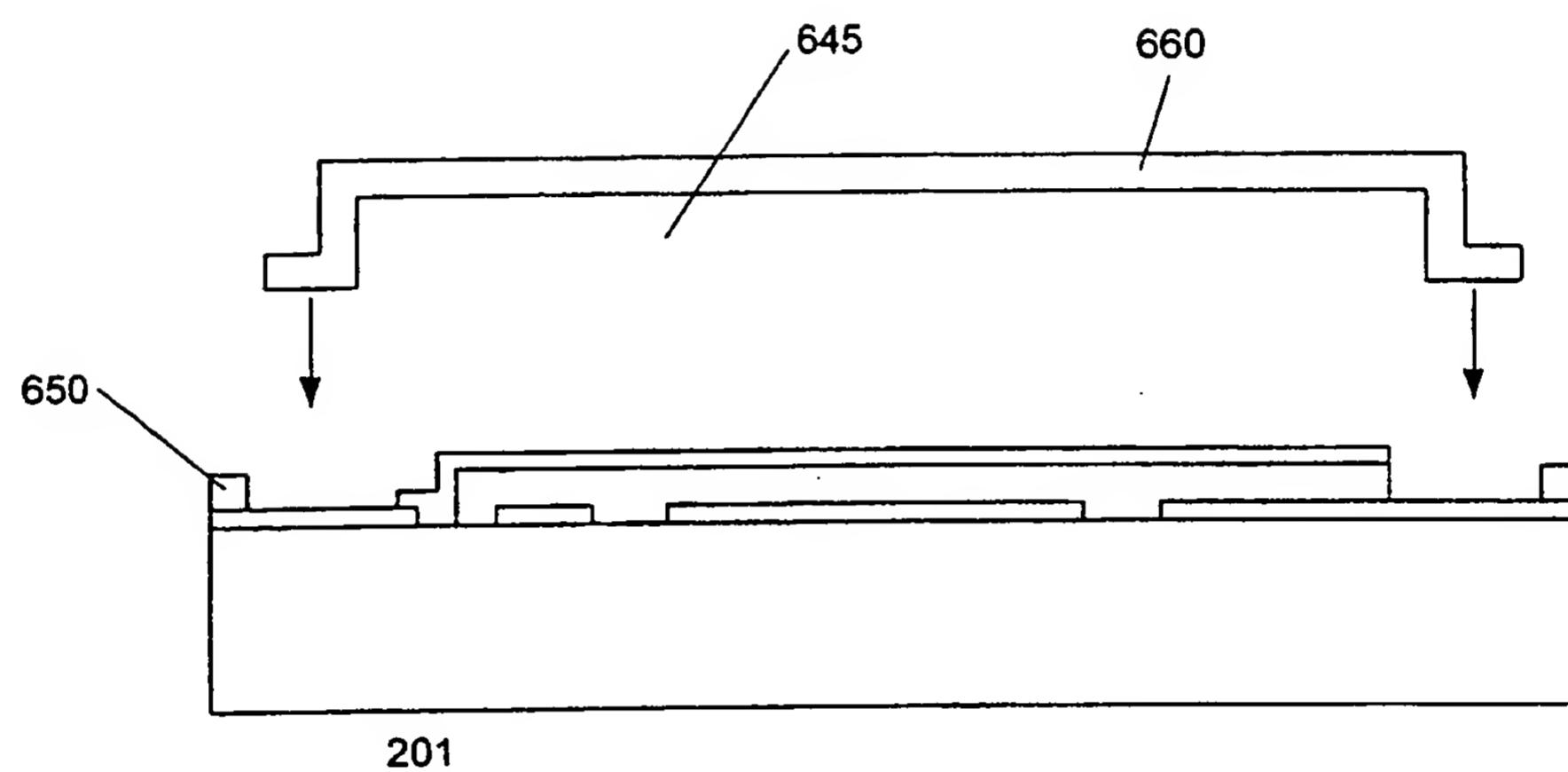


Fig. 6

INTERNATIONAL SEARCH REPORT

International application No.
PCT/SG 99/00144

CLASSIFICATION OF SUBJECT MATTER

IPC⁷: H01L 33/00

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC⁷: H01L, H05K

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EPODOC, WPI, PAJ, IEEE XPLOR

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	EP 0729191 A2 (EASTMAN KODAK COMPANY) 28 August 1996 (28.08.96) fig. 1; abstract; col. 3, lines 6-42; col. 3, line 55 - col. 4, line 53.	1-36
X	JP 09 97679 A (CASIO COMPUTER CO LTD) 8 April 1997 (08.04.97) (abstract). [online] [retrieved on 2000-09-06]. Retrieved from: EPO PAJ Database.	1-3, 5-18, 20, 21, 23, 25-36
X	Burrows, P.E. et al. Achieving full-color organic light-emitting devices for lightweight, flat-panel displays. Electron Devices, IEEE Transactions on, Volume: 44 Issue: 8, Aug. 1997. Page(s): 1188-1203. [online] [retrieved on 6 September 2000 (06.09.00)]. Retrieved from <URL: http://ieeexplore.ieee.org/iel/16/13266/00605453.pdf > figs. 1,3,10,12,17, pages 1188, 1192,1194	1-3,5-18,20,21,23,25-36

 Further documents are listed in the continuation of Box C. See patent family annex.

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Name and mailing address of the ISA/AT

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Information on patent family members

Patent document cited in search report			Publication date		Patent family member(s)		Publication date	
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